



Introductions

Me:

- Long-haul <u>Samba Team</u>
 Member (22 years)
- jCIFS project co-founder
- Book: <u>Implementing CIFS</u>
- Lead author of the SMB1 specs: [MS-CIFS], [MS-SMB]

A ruminant mammal (Geekus geekus) With humped shoulders, spindly legs, rollerblades, and broadly palmated antlers.



Introductions

The opinions expressed are my own and not necessarily those of:

- my spouse,
- ♠ my dog,
- ★ my spirit familiar,
- ★ my colleagues,
- the monster under the bed,
- ☆ or the basement bugs.

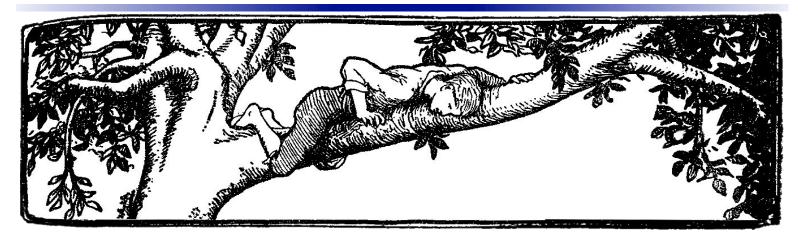












- TOE Cards: TCP Offload Engines
- iSCSI NICs: Networked SCSI
- SMB3 Offload: API Undefined



- SMB2 and SMB3 no SMB1
- Encryption / Compression
- Syntax Layer
 - Message Packing / Parsing
- Host-provided State
 - Authentication and Keys
 - Feature Support
- Fast I/O Processing







- Local Filesystem Interface
 - E.g.: POSIX Layer
 - Sync'd Access (Local, NFS, Object...)
- Metadata Management
 - o ACLs, Identity, Attributes
- File System Behaviors
 - Locking, Change Notify, etc.





The Semantic Layer

...runs above the offload engine.

- The SMBOE API needs to be useful, stable, and open.
- SMB2/3 implementations should all be able to use the same API.
- It should be possible to run different SMBOE implementations in parallel.

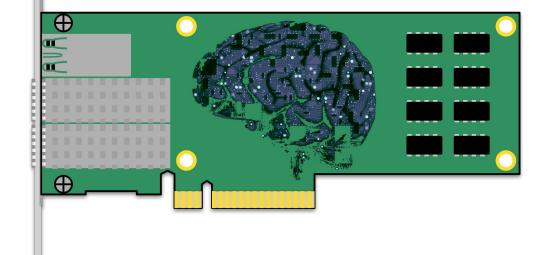


Building a Rational, Well-Documented API

- Stackable for adding new dialects, features, and capabilities.
- Device Driver / Library / Toolkit / All?
- It needs a community.

So...

SmartNICS



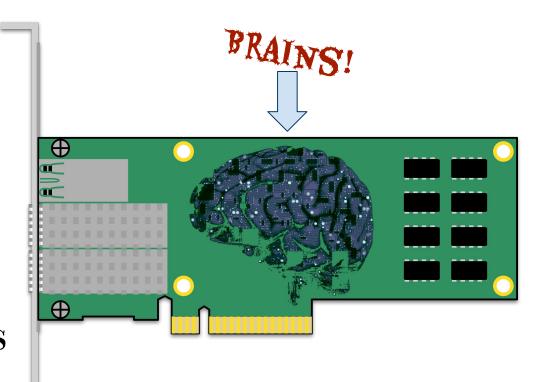




Network Interface Cards



with powerful processing capabilities





Network Interface Cards



with powerful processing capabilities

Programmable:

- >FPGA-based,
- >Multi-core ASICs, or
- ➤ A Combo of the above





Network
Interface
Cards
with

with powerful processing capabilities

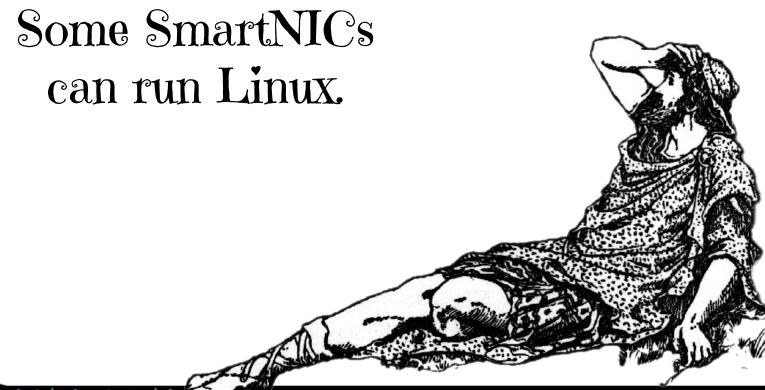
A generic networking offload device:

- >Multi-interface
- >Multi-physical layer
- >Multi-core processor
- **>**PCIe Interconnect

Ethernet, FC, NVMe-oF, RDMA...



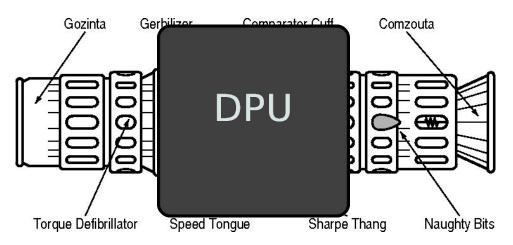






SmartNICs & DPUs

DPU: Data Processing Unit
...the FRAINS of the SmartNIC.



DPUs can also be placed on motherboards.



SmartNICs & DPUs

DPUs are typically RISC-based:

- ARM ARM
- MIPS
- RISC-V?

DPUs have lots of I/O capacity.







For convenience, I arranged the SMB3 commands into six debatably semi-logical categories:

Managing Connections • NEGOTIATE (0x0000) • SESSION_SETUP (0x0001) • LOGOFF (0x0002)	Share Access TREE_CONNECT (0x0003) TREE_DISCONNECT (0x0004)	Open/Close, Lock/Unlock • CREATE (0x0005) • CLOSE (0x0006) • LOCK (0x000A)
• ECHO (0x000D)		OPLOCK_BREAK (0x0012)
Fundamental I/O • READ (0x0008) • WRITE (0x0009) • FLUSH (0x0007)	Metadata Query and Set OUERY_DIRECTORY (0x000E) CHANGE_NOTIFY (0x000F) IOCTL (0x000B) QUERY_INFO (0x0010) SET_INFO (0x0011)	Odds and Ends • CANCEL (0x000C) • SMB2 Error Response



Several messages have the same format: typedef struct

```
uint16_t StructureSize;
uint8_t Reserved[2];
} smb2_BaseMsg;
```

- LOGOFF Request/Response
- TREE_DISCONNECT Request/Response
- ECHO Request/Response

- CANCEL Request
- LOCK Response
- FLUSH Response





Consider Create

- In SMB2/3, Create is a Monster.
 - Lots of flags fields
 - Access & Share Modes
 - Leases/OpLocks
 - Contexts

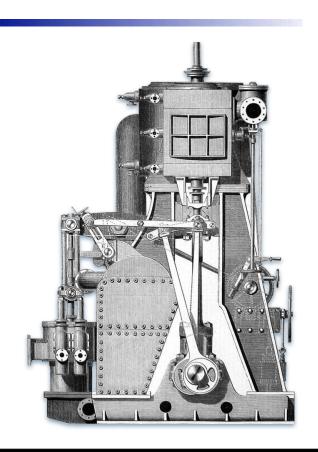
It's practically a sub-protocol. How should it be presented?





Short Term Goals:

- **★** Proxy Dæmon
- **★** Transform Headers
- **★** Unit Tests
- **★** Finish Messages (including Create)
- ★ Study other code





Where else might this code be useful?

- Software Defined Storage Switches
- Proxy and Cache Servers
- WAN Accelerators
- Hypervisors
- Remote AccessPortals





- LGPLv3 & AGPLv3
- Published modules are not well tested, but are fairly complete.
- ...and excessively well documented.



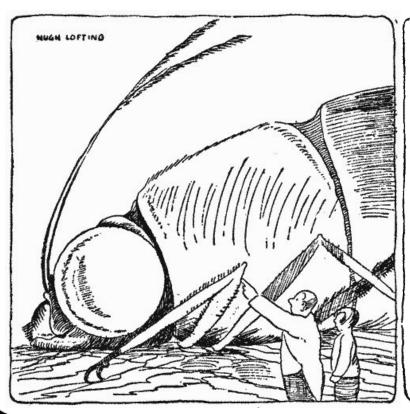


https://gitlab.com/ubiqx/zambezi





Distractions



In the '67 Doctor Dolittle movie...

The Giant Luna Moth is attracted by the light of the moon, and flies there. Once on the moon the Moth is attracted by the light of the earth, and so flies back again.

The cycle repeats.



Distractions

- SmartNIC and DPU Docs and Videos
 - I enjoy learning about all this new stuff
- Embedded Tinkering Platforms (RPi, etc.)
 - <u>Ultibo</u> for Raspberry Pi:
 - Development toolkit based on FreePascal
 - Built-in support for NTFS

Block Storage

• AoE is small and simple, so...



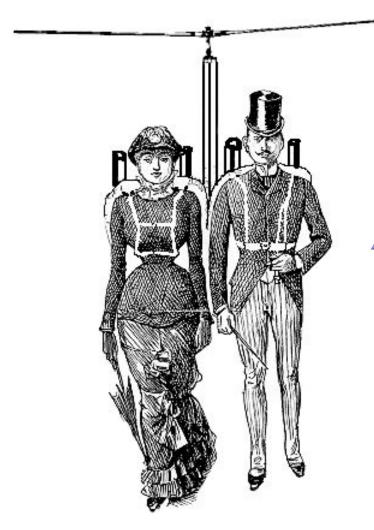




Project Zambezi

Goals:

- ★ Git 'er done.
- ★ Work with the SNIA
 - Standardize the API
 - Fork a reference implementation under an additional license
- ★ Partner with others to implement on SmartNICs
 - Find new & interesting use cases





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https://gitlab.com/ubiqx/ zambezi

The End



